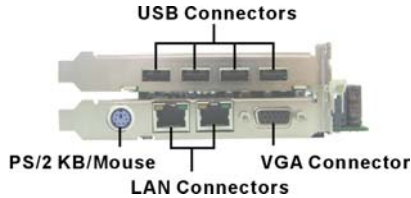
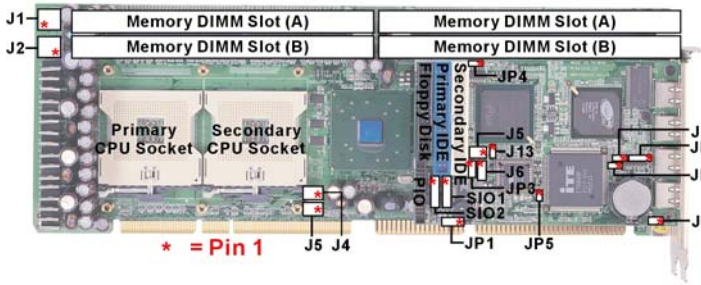




PEAK7220VL2G
Quick Reference Guide
Rev. B0 (PN: 1P7220VL00)



Jumper	Function	Jumper	Function
J1/J2	Power Connector	JP1	Speaker
J4/J5	Fan Connector	JP3	GPIO
J5 on Daughter Board	ATX Power	JP4	On-board RTC
		JP5	SMBUS
		JP6	Reset Button
J6	AC'97	JP7	Power Button
J7	IDE Active LED	JP9	Keyboard Lock
J13	AT/ATX Selection		

J1/J2: Power Connector

1	GND	2	GND
3	+12V	4	+12V

J4/J5: FAN Connector

1	GND	2	+12V
3	Sense		

J13: AT/ATX Selection

1-2 short	ATX mode
*2-3 short	AT mode

JP1: Speaker

1	Speaker	2	GND
3	GND	4	+5V

JP4: On-board RTC

*1-2short	Operation Mode
2-3 short	Clear CMOS

JP5: External SMBUS

1	SMB_CLK
2	SMB_DAT

JP6: Reset Button

1	Ground
2	Reset

JP7: Power Button

1	Power Up
2	Ground

JP9: Keyboard Lock

1	KL_VCC
2	NC
3	GND
4	KEYLOCK
5	GND

Note: 1. When users only install one CPU, use the Primary CPU Socket.

2. The memory modules should be installed in pairs. Use Slot A and/or Slot B for convenience.

JP3: GPIO Port

1	GP27_D_IN1	2	GP23_D_OUT1
3	GP26_D_IN2	4	GP22_D_OUT2
5	GP25_D_IN3	6	GP21_D_OUT3
7	GP24_D_IN4	8	GP20_D_OUT4

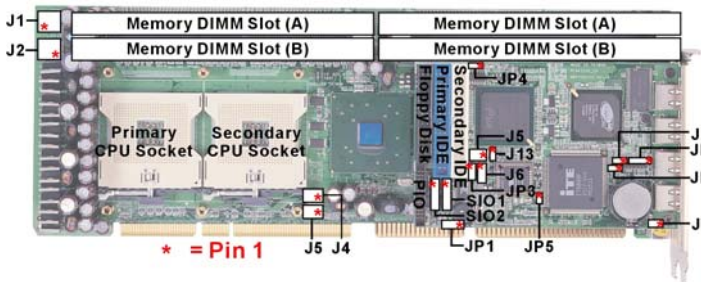
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7	GP24_D_IN4	8	GP20_D_OUT4

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